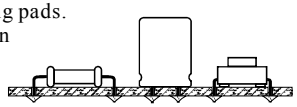
Soldering Hints

① Put leads through mounting holes from the side with part outline. Ensue component evenly touch PCB.
② Solder leads at the other side. Solder should fully fill and cover soldering pads. Avoid bridges between neighbering pads.
③ Cut unused leads flush with cutter.



DSO 138 Oscilloscope DIY Kit

User Manual

Rev. 01

Tools you need

① Iron (20W) ④ Screw driver
② Solder wire ⑤ Flush cutter
③ Multimeter ⑥ Tweezers

Before you start

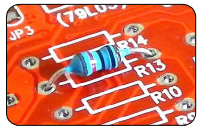
① Check part values & quantities against part list
② Always meter resistor values before soldering
③ Understand all part polarities and orientations

Step 1

Assembly Main Board and LCD board (follow the order as numbered)

*** These parts are optional and not required for the normal oscilloscope function.**

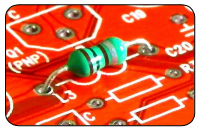
1. Resistors



Note:
Always meter resistor values before soldering

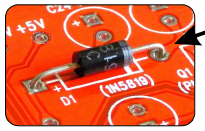
- ☐ R1, R14, R16 : 100K Ω
- ☐ R2 : 1.8M Ω
- ☐ R3 : 200K Ω
- ☐ R4 : 2M Ω
- ☐ R5 : 20K Ω
- ☐ R6 : 300 Ω
- ☐ R7, R36 : 180 Ω
- ☐ R8, R12, R13 : 120 Ω
- ☐ R9, R15, R26 : 1K Ω
- ☐ R10 : 3K Ω
- ☐ R11, R38 : 1.5K Ω
- ☐ R28, R40 : 470 Ω
- ☐ R37, R39 : 10K Ω

2. HF-Chokes



☐ L1,L3,L4 : 100 μ H

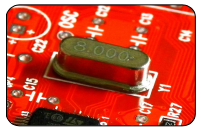
3. Diodes



Cathode

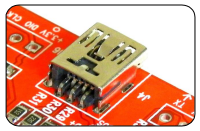
- ☐ D1 : 1N5819
- ☐ D2 : 1N4004 (or 1N4007)

4. Crystal



☐ Y1 : 8MHz

5. USB Socket *



☐ J4 : USB mini - B

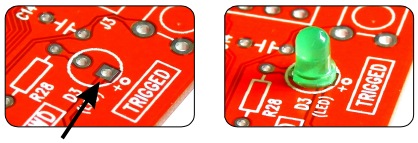
6. Tact Switches

☐ SW4, SW5, SW6, SW7, SW8 : 6 X 6 X 5mm

7. Ceramic Capacitors

- ☐ C1, C9, C10, C11, C14, C15, C16, C17, C18, C20, C23 : 0.1 μ F
- ☐ C2 : 330pF
- ☐ C3 : 3pF
- ☐ C5 : 1pF
- ☐ C7, C8 : 120pF
- ☐ C12, C13 : 22pF

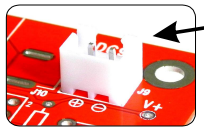
8. LED



Solder positive pole (the longer lead) to the square pad

☐ D3 : φ 3mm, green

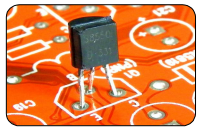
9. Pin header (for power)



Face the opening outward

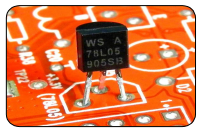
☐ J9 : 2 Pin

10. Transistors



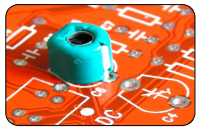
- ☐ Q1 : 8550
- ☐ Q2 : 9014

11. Regulators



- ☐ U4 : 79L05
- ☐ U5 : 78L05

12. Capacitor trimmers

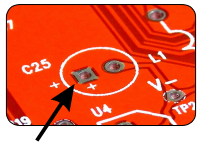


☐ C4, C6 : 5 - 30pF

13. Power inductor

☐ L2 : 1mH/0.5A

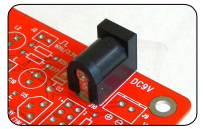
14. Electrolytic capacitors



Solder positive pole (the longer lead) to the square pad

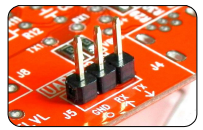
☐ C19, C21, C22, C24, C25, C26 : 100 μ F / 16V

15. Power connector



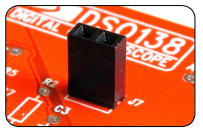
☐ J10 : DC005

16. Pin-header (male) *



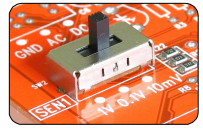
- ☐ J5 : 1 X 3 pin
- ☐ J6 : 1 X 4 pin

17. Pin-header (female)



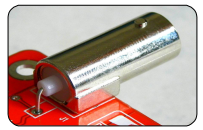
- ☐ J7, J8 : 1 X 2 pin
- ☐ J3 : 2 X 20 pin

18. Slide switches



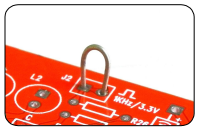
☐ SW1, SW2, SW3 : 2P3T

19. BNC connector



☐ J1 : BNC

20. Test signal ring



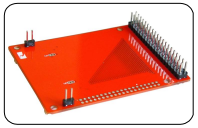
1) Make a small ring with a lead cut-off.

2) Solder the ring to the two holes of J2 (as shown in the photo).

21. JP3

Short JP3 with solder

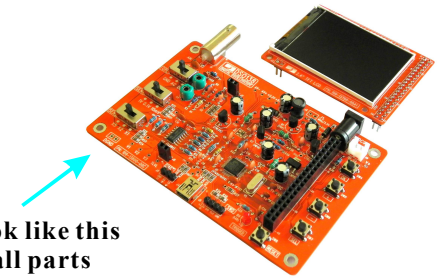
22. LCD Board



Note: Install to the side opposite to LCD panel.

☐ J1 : 2 X 20 pin

☐ J2, J3 : 1 X 2 pin



The assembly should look like this after you have finished all parts

Step 2 Test and Use

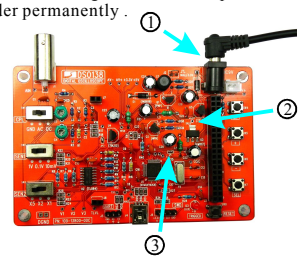
NOTE: You need a 9V DC power supply (at least 200mA capacity) to run the scope. This power supply is not included in the kit.

A. Check voltages

① Apply 9V power to J10 (or J9).

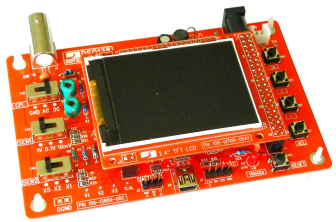
② Check voltage at TP22. It should be around +3.3V.

③ If voltage at TP22 is good disconnect power. Short JP4 with solder permanently .



B. Attach LCD board

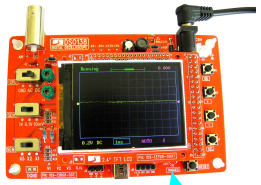
Plug LCD board into the female headers J3, J7, and J8 on the main board.



C. Verify

① Connect power supply again. You should see LCD lights up and oscilloscope panel displayed.

② Press various buttons and move switches to verify their functions.

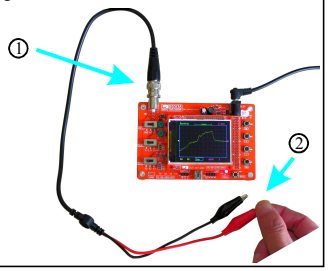


"Trigger" LED blinking twice indicates booting-up is good.

A. Use

① Attach probe clips to J1.

② Touch the red clip with your finger. Do you see signal from your finger?



Troubleshooting

LCD Dark (No backlight)

- Is voltage at V+ good? — No → Check power supply
- Yes → Is R36 value correct and soldered good? — No → Fix R36
- Yes → Do you get about 3V between J1 pin 16 & 18 on LCD board? — No → Check R36 and power again
- Yes → Check LCD board

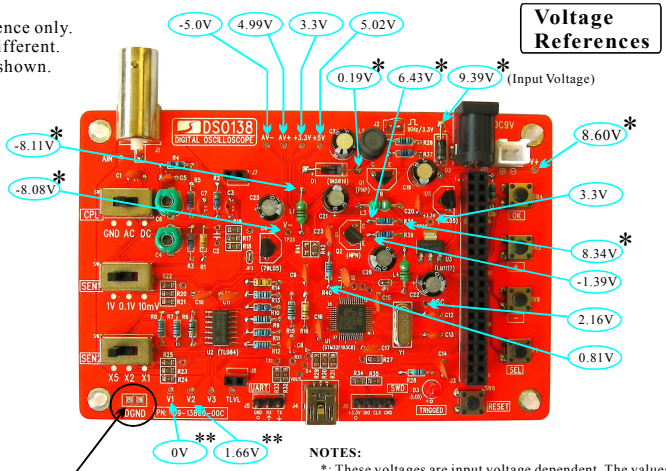
No Display

- Short JP4 if it has not been done. See Step 2 above.
- Press SW8. Does LED blink? — No →
  - Check +3.3V voltage
  - Check Y1, C12, C13
  - Check LED installation
- Yes →
  - Check J3 soldering for possible opens or shorts
  - Check J1 soldering on LCD board for possible opens or shorts

No Trace

- Are the values of AV+ and AV- correct? — No → Fix them
- Yes → Set CPL switch to GND and measure V1 and V2. Are they correct? — No → Check U2B, U2C and related parts around these two amplifiers
- Yes → Check R12 and C8

NOTE: The voltages in the photo are for reference only. The voltages on your board could be different. But they should be close to the values shown.



Place the negative pen of volt-meter here to do voltage measurements.

NOTES:

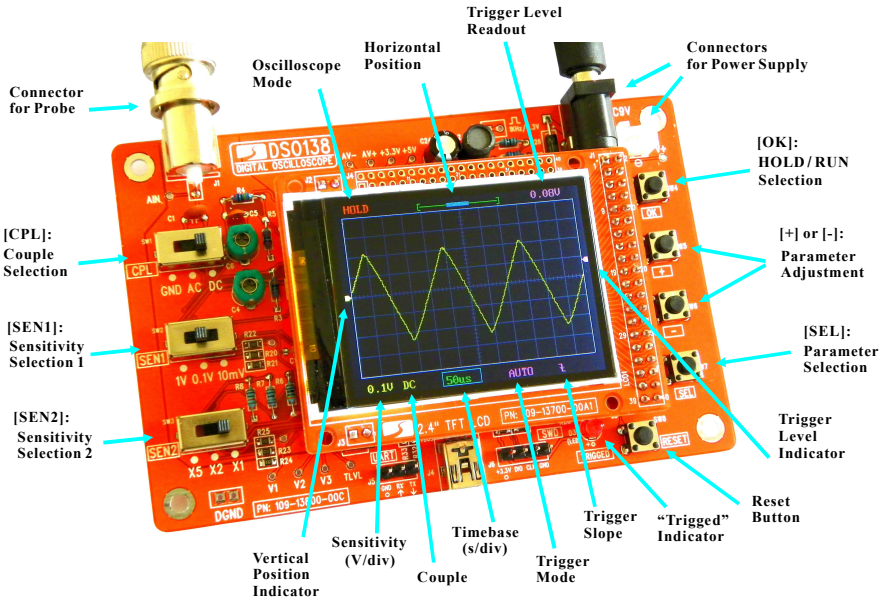
*: These voltages are input voltage dependent. The values shown were measured when input voltage was 9.39V.

**: These voltages are measured when CPL switch (SW1) is set to GND position.

Tech Support: www.jyetech.com/forum

How to Use

Display and Controls



Connections

Power Supply: Connect DC power supply to J9 or J10. The power supply voltage must be in the range of 8 - 12V.

Probe: Connect probe to J1.

Attention

1. Power supply voltage must not exceed 12V. Otherwise U5 will get hot.
2. Allowed maximum signal input voltage is 50Vpk (100Vpp) with the clip probe.

Operations

Press on [SEL] button: Select parameter to be adjusted. The selected parameter will be highlighted.

Press on [+] or [-] button: Adjust the parameter selected by [SEL] button.

Press on [OK] button: Freeze waveform refresh (entering HOLD state). Press on it again will de-freeze.

Change [CPL] switch: Set couple to DC, AC, or GND. When GND is selected the scope input is isolated from input signal and connected to ground (0V input).

Change [SEN1] or [SEN2] switch: Adjust sensitivity. The product of [SEN1] and [SEN2] settings makes the actual sensitivity which is displayed at the lower-left corner of the panel.

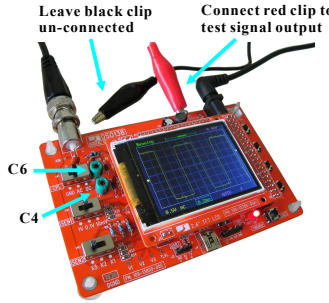
Press on [Reset] button: Perform a system reset and re-boots the oscillscope.

0V Line Alignment

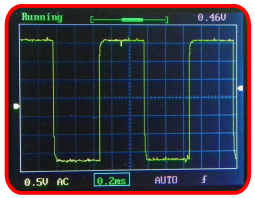
Sometimes you may find the 0V line (the trace corresponding to 0V input voltage) does not match with the VPOS indicator at the screen left border. This can easily be fixed by performing the "0V line alignment" function. First, set the couple switch [CPL] to GND position. Then press on [SEL] button to make VPOS indicator highlighed and hold down [OK] button for about 2 seconds. You will set the trace aligned to VPOS indicator when you release [OK] button. You may see some residue mismatch remains at the highest sensitivity settings. This is normal.

Probe Calibration

Because there is always some capacitance between scope input and ground probe needs to be calibrated to achieve better measurement results for high frequency signals. This can be done with the help of the built-in test signal. To do this please follow the steps below.

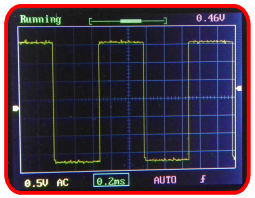


1. Connect the red clip to the test signal terminal and leave the black clip un-connected (see photo at right).
2. Set [SEN1] switch to 0.1V and [SEN2] switch to X5. Set [CPL] switch to AC or DC.
3. Adjust timebase to 0.2ms. You should see waveform similar to that shown in photos below. If traces are not stable adjust trigger level (the pink triangle on right screen border) so as you get a stable display.
4. Turn C4 (capacitor trimmer) with a small screw driver so that the waveform displays sharp rightangle (photo C).
5. Set [SEN1] switch to 1V and [SEN2] switch to X1while keep all other settings unchanged. Adjust C6 so that sharp rightangle waveform is displayed.



A – Not enough

B – Too much



C – Good

Hints

The LED at bottom-right corner (labelled "TRIGGED") is the trigger indicator. It blinks when triggers are detected.

Triggers and Their Modes

Triggers are events that indicate signal voltage acrossing a set level (i.e. trigger level) along a specified direction (i.e. trigger slope, rising or falling). Oscilloscope uses triggers as reference points in time for stable waveform display and measurements.

Auto Mode

In auto mode oscilloscope will perform display refresh no matter triggers happen or not. When triggers are detected waveform display will be displayed with reference to trigger points. Otherwise, display waveform at ramdom reference points.

Normal Mode

In normal mode oscilloscope will only perform display refresh when there are triggers. If no triggers happen waveform display will stay unchanged.

Single Mode

Single mode is the same as normal mode except that oscilloscope will enter HOLD state after a trigger has been detected and waveform display has been updated.

Normal and single modes are useful for capturing sparse or single waveform.

Specifications	
Max realtime sample rate	1MSa/s
Analog bandwidth	0 -- 200KHz
Sensitivity range	10mV/div - 5V/div
Max input voltage	50Vpk (1X probe)
Input impedance	1M ohm/20pF
Resolution	12 bits
Record length	1024 points
Timebase range	500s/Div -- 10us/Div
Trigger modes	Auto, Normal, and Single
Trigger position range	50%
Power supply	9V DC (8 – 12V)
Current consumption	~120mA
Dimension	117 x 76 x 15mm
Weight	70 gram (without probe)

Tech Support: ***www.jyetech.com/forum***

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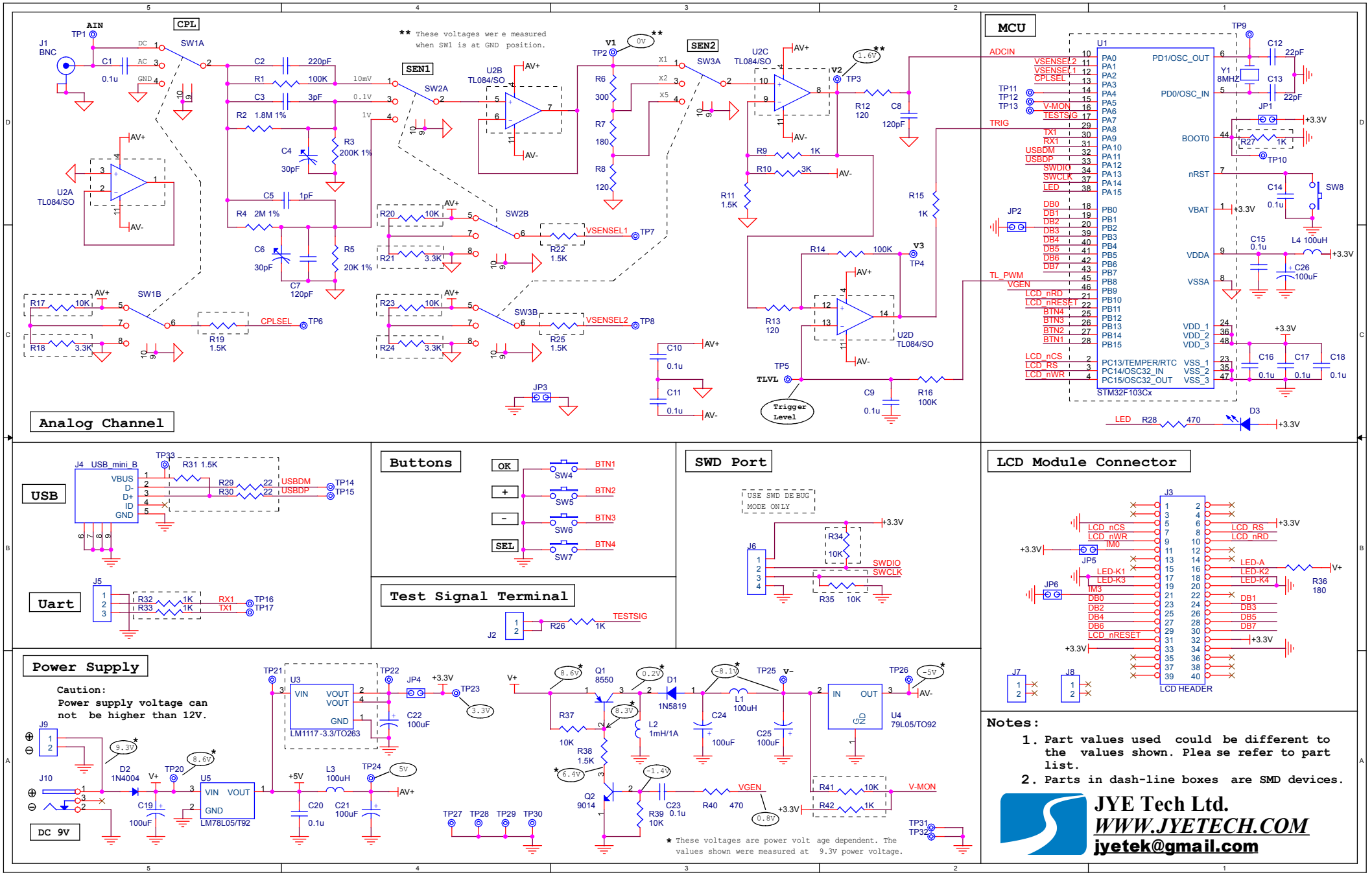
Analog Channel
CPL
SEN1
SEN2
** These voltages wer e measured when SW1 is at GND position.
AIN
J1 BNC
TP1
SW1A
C1 0.1u
C2 220pF
R1 100K
C3 3pF
R2 1.8M 1%
R3 200K 1%
C4 30pF
C5 1pF
R4 2M 1%
R5 20K 1%
C6 30pF
C7 120pF
SW2A
SW2B
SW3A
SW3B
SW1B
U2A TL084/SO
U2B TL084/SO
U2C TL084/SO
U2D TL084/SO
R6 300
R7 180
R8 120
R9 1K
R10 3K
R11 1.5K
R12 120
C8 120pF
R13 120
R14 100K
R15 1K
R16 100K
C9 0.1u
C10 0.1u
C11 0.1u
R17 10K
R18 3.3K
R19 1.5K
CPLSEL
TP6
R20 10K
R21 3.3K
R22 1.5K
VSENSEL1
TP7
R23 10K
R24 3.3K
R25 1.5K
VSENSEL2
TP8
JP3
V1
V2
V3
TP2
TP3
TP4
TP5
TLVL
Trigger Level
MCU
U1
STM32F103Cx
ADCIN
VSENSEL2
VSENSEL1
CPLSEL
V-MON
TESTSIG
TRIG
TX1
RX1
USBDM
USBDP
SWDIO
SWCLK
LED
TL_PWM
VGEN
LCD_nRD
LCD_nRESET
BTN4
BTN3
BTN2
BTN1
LCD_nCS
LCD_RS
LCD_nWR
TP9
Y1 8MHZ
C12 22pF
C13 22pF
JP1
R27 1K
TP10
BOOT0
nRST
C14 0.1u
SW8
VBAT
C15 0.1u
L4 100uH
VDDA
C26 100uF
VSSA
C16 0.1u
C17 0.1u
C18 0.1u
JP2
USB
J4 USB_mini_B
TP33
R31 1.5K
R29 22
R30 22
TP14
TP15
Uart
J5
R32 1K
R33 1K
TP16
TP17
Buttons
OK
+
-
SEL
SW4
SW5
SW6
SW7
BTN1
BTN2
BTN3
BTN4
Test Signal Terminal
J2
R26 1K
TESTSIG
SWD Port
USE SWD DEBUG MODE ONLY
J6
R34 10K
R35 10K
SWDIO
SWCLK
LCD Module Connector
J3
LCD HEADER
JP5
JP6
R36 180
LED-A
LED-K1
LED-K2
LED-K3
LED-K4
IM0
IM3
DB0
DB1
DB2
DB3
DB4
DB5
DB6
DB7
J7
J8
Power Supply
Caution:
Power supply voltage can not be higher than 12V.
DC 9V
J9
J10
D2 1N4004
C19 100uF
U5
LM78L05/T92
U3
LM1117-3.3/TO263
C22 100uF
JP4
TP23
C20 0.1u
C21 100uF
L3 100uH
TP24
TP21
TP22
TP20
Q1 8550
D1 1N5819
L1 100uH
L2 1mH/1A
C24 100uF
C25 100uF
U4 79L05/TO92
TP25
TP26
R37 10K
R38 1.5K
Q2 9014
C23 0.1u
R39 10K
R40 470
R41 10K
R42 1K
V-MON
VGEN
TP27
TP28
TP29
TP30
TP31
TP32
★ These voltages are power volt age dependent. The values shown were measured at 9.3V power voltage.
Notes:
1. Part values used could be different to the values shown. Plea se refer to part list.
2. Parts in dash-line boxes are SMD devices.
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